V.S.R Govt. Degree & P.GCollege-Movva Affiliated to Krishna University, Machilipatnam.

Department of Electronics

S.No.	Subject	Sem	Date	Торіс	No of studen ts	Name of the Lecturer
				2018-2019		
1	Electronics	Ι	02-07-2018	Average Value and RMS Value of Alternating Current	23	S.KIRANMAYI
	"	Ι	02-07-2018	Differences Between AC and DC	23	"
	"	Ι	04-07-2018	Branch current method, Nodal Analysis	23	"
	"	Ι	04-07-2018	Star to Delta & delta to star	23	"
	"	Ι	20-07-2018	Thevenins Theorem	23	"
	"	Ι	20-07-2018	Maximum Power Transfer Theorem	23	"
	"	Ι	21-07-2018	Frequency response of RC & RL circuits	23	"
	"	Ι	21-07-2018	Passive Differentiating and Integrating circuits	23	"
	"	Ι	28-08-2018	parallel Resonance circuits	23	"
	"	Ι	28-08-2018	Tank circuits - LC Oscillations	23	"
2	Electronics	II	30-11-2018	Working of forward and reverse bias conditions of PN junction diode	18	S.KIRANMAYI
	"	Π	30-11-2018	Construction, Working, V-I Characteristics of Zener Diode	18	"
	"	Π	01-12-2018	Construction, Operation, Characteistics of CE Configuration	18	"
	"	Π	01-12-2018	Voltage Divider Bias	18	"
	"	Π	07-02-2019	Construction, Operation, Characteistics of JFET	18	"
	"	Π	07-02-2019	Structure and Working ,Characteristics of SCR	18	"
	"	Π	26-02-2019	Structure and operation of LDR	18	"
	"	Π	26-02-2019	Light Emitting Diode	18	"
	"	Π	13-02-2019	Construction and working of Full wave Bridge rectifier	18	"
	"	Π	13-02-2019	Principle and working of SMPS	18	"
3	Electronics	III	02-07-2018	Number conversion from one no.system to another no.system	16	S.KIRANMAYI
	"	III	02-07-2018	1's,2's,9's,10's complements of addition, subtraction	16	"

	"	III	04-07-2018	Demorgans Laws	16	"
	"	III	04-07-2018	Karnaugh map method 4,5 variables	16	"
	"	III	20-07-2018	Half Adder and Full Adder	16	"
	"	III	20-07-2018	TTL Logic families	16	"
	"	III	21-07-2018	Master- Slave JK Flip Flop	16	"
	"	III	21-07-2018	Mod 16 Asynchronous Counter	16	"
	"	III	28-08-2018	Memory operation of ROM,RAM	16	"
	"	III	28-08-2018	PLA and PAL	16	"
4	Electronics	IV	30-11-2018	Block diagram of OP-Amp	16	S.KIRANMAYI
	"	IV	30-11-2018	working of Inverting and Non Inverting OP-Amp	16	"
	"	IV	01-12-2018	Op-Amp as Voltage Regulator	16	"
	"	IV	01-12-2018	Op-Amp as Square wave generators	16	"
	"	IV	07-02-2019	BCD to Seven Segment Display decoder	16	"
	"	IV	07-02-2019	Universal Shift Register	16	"
	"	IV	26-02-2019	Successive Approximation type ADC	16	"
	"	IV	26-02-2019	R-2R Ladder network DAC	16	"
	"	IV	13-02-2019	Digital clock	16	"
	"	IV	13-02-2019	Universal Asynchronous receiver transmitter	16	"
5	Electronics	V P5	02-07-2018	Functional Block diagram of Intel 8085	22	S.KIRANMAYI
	"	V P5	02-07-2018	Interrupts and priority concept	22	"
	"	V P5	04-07-2018	Instruction set of 8085	22	"
	"	V P5	04-07-2018	Timing diagram for opcode fetch	22	"
	"	V P5	20-07-2018	Alp for addition of two 8 bit numbers	22	"
	"	V P5	20-07-2018	Time delay using single and double register	22	"
	"	V P5	21-07-2018	2K X 8 ROM memory interfacing	22	"
	"	V P5	21-07-2018	Differences between I/O mapped and memory mapped I/O	22	"
	"	V P5	28-08-2018	Programmable peripheral devices 8255	22	"
	"	V P5	28-08-2018	stepper motor control Interface	22	"
	"	V P6	02-07-2018	Block diagram of communication system	22	S.KIRANMAYI
	"	V P6	02-07-2018	Noise in communication system	22	"
	"	V P6	04-07-2018	Transistor modulator	22	"

	"	V P6	04-07-2018	DSBSC generation	22	"
	"	V P6	20-07-2018	FET Reactance modulator	22	"
	"	V P6	20-07-2018	Comparision Between AM,FM and PM	22	"
	"	V P6	21-07-2018	Block diagram of AM Transmitter	22	"
	"	V P6	21-07-2018	Super heterodyne AM Receiver	22	"
	"	V P6	28-08-2018	Pulse amplitude modulation	22	"
	"	V P6	28-08-2018	Delta modulation	22	"
6	Electronics	VI	30-11-2018	Block diagram of 8051 microcontroller	22	S.KIRANMAYI
	"	VI	30-11-2018	memory organization of 8051	22	"
	"	VI	01-12-2018	Classification of instruction set	22	"
	"	VI	01-12-2018	Alp for addition of two 8 bit numbers	22	"
	"	VI	07-02-2019	Keyboard and display Interfacing	22	"
	"	VI	07-02-2019	ADC interfacing	22	"
	"	VI	26-02-2019	160CXX PIC Controller	22	"
	"	VI	26-02-2019	Introduction to Embedded Systems	22	"
7	Electronics	Cluste	30-11-2018	Block diagram of measurement system	22	S.KIRANMAYI
	"	Cluste	30-11-2018	working of ammeter	22	"
	"	Cluste	01-12-2018	De Sauty bridge	22	"
	"	Cluste	01-12-2018	Basic Principles of phase locked loop	22	"
	"	Cluste	07-02-2019	Function generator	22	"
	"	Cluste	07-02-2019	Block diagram of Spectrophoto meter	22	"
	"	Cluste	26-02-2019	Principle, working of Ph meter	22	"
	"	Cluste	26-02-2019	Working of Thermometer	22	"
	"	Cluste	13-02-2019	Direct digital control	22	"
	"	Cluste	13-02-2019	Block diagram of PLC's	22	"
8	Electronics	Cluste	30-11-2018	Need for semiconductor power devices	22	S.KIRANMAYI
	"	Cluste	30-11-2018	Structure of SCR	22	"
	"	Cluste	01-12-2018	Structure, working and V-I Characteristics of Diac	22	"
	"	Cluste	01-12-2018	Differences between SCR and Diac	22	"
	"	Cluste	07-02-2019	Step- down chopper	22	"
	"	Cluste	07-02-2019	DC Chopper circuits using self commutation	22	"

	"	Cluste	26-02-2019	parallel capacitor commuted invertors with reactive feedback	22	"
	"	Cluste	26-02-2019	Thyristor based speed control of DC motors	22	"
	"	Cluste	13-02-2019	AC induction motor	22	"
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9	Electronics	Ι	03-09-2019	Differences Between AC and DC	9	S.KIRANMAYI
	"	Ι	03-09-2019	Branch current method, Nodal Analysis	9	"
	"	Ι	06-09-2019	Average Value and RMS Value of Alternating Current	9	"
	"	Ι	06-09-2019	Tank circuits - LC Oscillations	9	"
	"	Ι	16-09-2019	Star to Delta & delta to star	9	"
	"	Ι	16-09-2019	parallel Resonance circuits	9	"
	"	Ι	26-09-2019	Thevenins Theorem	9	"
	"	Ι	26-09-2019	Passive Differentiating and Integrating circuits	9	"
	"	Ι	21-09-2019	Maximum Power Transfer Theorem	9	"
	"	Ι	21-09-2019	Frequency response of RC & RL circuits	9	"
10	Electronics	II	06-02-2020	Working of forward and reverse bias conditions of PN junction diode	8	S.KIRANMAYI
	"	Π	06-02-2020	Construction, Operation, Characteistics of CE Configuration	8	"
	"	II	11-02-2020	Construction, Working, V-I Characteristics of Zener Diode	8	"
	"	Π	11-02-2020	Construction, Operation, Characteistics of JFET	8	"
	"	Π	12-02-2020	Voltage Divider Bias	8	"
	"	Π	12-02-2020	Structure and operation of LDR	8	"
	"	Π	13-02-2020	Structure and Working ,Characteristics of SCR	8	"
	"	Π	13-02-2020	Construction and working of Full wave Bridge rectifier	8	"
	"	Π	07-03-2020	Light Emitting Diode	8	"
	"	Π	11-03-2020	Principle and working of SMPS	8	"
11	Electronics	III	03-09-2019	Number conversion from one no.system to another no.system	19	S.KIRANMAYI
	"	III	03-09-2019	Demorgans Laws	19	"
	"	III	06-09-2019	1's,2's,9's,10's complements of addition,subtraction	19	"
	"	III	06-09-2019	Half Adder and Full Adder	19	"
	"	III	16-09-2019	Karnaugh map method 4,5 variables	19	"
	"	III	16-09-2019	Master- Slave JK Flip Flop	19	"
	"	III	26-09-2019	TTL Logic families	19	"

	"	III	26-09-2019	Memory operation of ROM,RAM	19	"
	"	III	21-09-2019	Mod 16 Asynchronous Counter	19	"
	"	III	21-09-2019	PLA and PAL	19	"
12	Electronics	IV	06-02-2020	Block diagram of OP-Amp	19	S.KIRANMAYI
	"	IV	06-02-2020	Op-Amp as Voltage Regulator	19	"
	"	IV	11-02-2020	Op-Amp as Square wave generators	19	"
	"	IV	11-02-2020	working of Inverting and Non Inverting OP-Amp	19	"
	"	IV	12-02-2020	Universal Shift Register	19	"
	"	IV	12-02-2020	Successive Approximation type ADC	19	"
	"	IV	13-02-2020	BCD to Seven Segment Display decoder	19	"
	"	IV	13-02-2020	Successive Approximation type ADC	19	"
	"	IV	07-03-2020	R-2R Ladder network DAC	19	"
	"	IV	11-03-2020	Digital clock	19	"
13	Electronics	V P5	03-09-2019	Functional Block diagram of Intel 8085	13	S.KIRANMAYI
	"	V P5	03-09-2019	Interrupts and priority concept	13	"
	"	V P5	06-09-2019	Timing diagram for opcode fetch	13	"
	"	V P5	06-09-2019	Instruction set of 8085	13	"
	"	V P5	16-09-2019	Time delay using single and double register	13	"
	"	V P5	16-09-2019	Alp for addition of two 8 bit numbers	13	"
	"	V P5	26-09-2019	2K X 8 ROM memory interfacing	13	"
	"	V P5	26-09-2019	stepper motor control Interface	13	"
	"	V P5	21-09-2019	Programmable peripheral devices 8255	13	"
	"	V P5	21-09-2019	Differences between I/O mapped and memory mapped I/O	13	"
	"	V P6	03-09-2019	Block diagram of communication system	13	S.KIRANMAYI
	"	V P6	03-09-2019	Transistor modulator	13	"
	"	V P6	06-09-2019	DSBSC generation	13	"
	"	V P6	06-09-2019	Noise in communication system	13	"
	"	V P6	16-09-2019	Comparision Between AM,FM and PM	13	"
	"	V P6	16-09-2019	FET Reactance modulator	13	"
	"	V P6	26-09-2019	Super heterodyne AM Receiver	13	"
	"	V P6	26-09-2019	Delta modulation	13	"

	"	V P6	21-09-2019	Pulse amplitude modulation	13	"
	"	V P6	21-09-2019	Block diagram of AM Transmitter	13	"
14	Electronics	VI	06-02-2020	Block diagram of 8051 microcontroller	14	S.KIRANMAYI
	"	VI	06-02-2020	Classification of instruction set	14	"
	"	VI	11-02-2020	Alp for addition of two 8 bit numbers	14	"
	"	VI	11-02-2020	memory organization of 8051	14	"
	"	VI	12-02-2020	Keyboard and display Interfacing	14	"
	"	VI	12-02-2020	Introduction to Embedded Systems	14	"
	"	VI	07-03-2020	160CXX PIC Controller	14	"
	"	VI	11-03-2020	ADC interfacing	14	"
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15	Electronics	Ι	19-11-2020	Difference between AC and DC	27	S.KIRANMAYI
	"	Ι	19-11-2020	Star to deltaδ to star conversion	27	"
	"	Ι	05-12-2020	Thevinen's theorem	27	"
	"	Ι	05-12-2020	Maximum power transfer Theorem	27	"
	"	Ι	10-12-2020	Frequency response of RC & RL circuits	27	"
	"	Ι	10-12-2020	Low pass and high pass filters	27	"
	"	Ι	18-12-2020	Working and characteristics of JFET And MOSFET	27	"
	"	Ι	18-12-2020	Half wave and full wave rectifiers	27	"
	"	Ι	29-12-2020	light Emitting Diode	27	"
	"	Ι	29-12-2020	Light dependent resistor(LDR)	27	"
16	Electronics	II	11-01-2021	Decimal, binary, octal, hexa decimal number system	25	S.KIRANMAYI
	"	II	20-01-2021	1's,2's,9's,10's complements of addition, subtraction	25	"
	"	II	23-01-2021	De-morgan's theorem	25	"
	"	II	06-02-2021	NAND And NOR gates are universal gates	25	"
	"	II	15-02-2021	Half adder and Full Adder	25	"
	"	II	19-02-2020	Multiplexer and demultiplexer	25	"
	"	II	22-02-2021	J-K Flip flop and master-slave flip flop	25	"
	"	II	22-02-2021	PLA and PAL	25	"
	"	Π	26-02-2021	Encoder and Decoder	25	"
	"	Π	26-02-2021	BCD to Seven Segment Display decoder	25	"

17	Electronics	III	19-11-2020	Block diagram of OP-Amp	8	S.KIRANMAYI
	"	III	19-11-2020	OP-Amp as Inverting and non-inverting	8	"
	"	III	05-12-2020	OP-Amp as voltage Regulator	8	"
	"	III	05-12-2020	IC-555 Functional block diagram	8	"
	"	III	10-12-2020	Need for modulation	8	"
	"	III	10-12-2020	Diode detector	8	"
	"	III	18-12-2020	Mathematical representation of FM wave	8	"
	"	III	18-12-2020	Phase locked loop(PLL)	8	"
	"	III	29-12-2020	Super heterodyne AM Receiver	8	"
	"	III	29-12-2020	Difference between AC and DC	8	"
18	Electronics	IV P4	11-01-2021	INTEL 8085 Architecture	6	S.KIRANMAYI
	"	IV P4	20-01-2021	Pin diagram of 8085	6	"
	"	IV P4	23-01-2021	Addressing modes of 8085	6	"
	"	IV P4	06-02-2021	alp for addition of two 8 bit numbers	6	"
	"	IV P4	15-02-2021	Serial communication interface (8251-USART)	6	"
	"	IV P4	19-02-2020	Pin description of 8086	6	"
	"	IV P4	22-02-2021	Architecture of 8086	6	"
	"	IV P4	22-02-2021	ARM Architecture and organization	6	"
	"	IV P4	26-02-2021	16/32 bit processor	6	"
	"	IV P4	26-02-2021	Program status word(PSW)	6	"
	"	IV P5	11-01-2021	Difference between micro proccesor and micro controlller	6	S.KIRANMAYI
	"	IV P5	20-01-2021	Pin diagram of 8051	6	"
	"	IV P5	23-01-2021	Block diagram of 8051 micro controller	6	"
	"	IV P5	06-02-2021	control of Stepper motor	6	"
	"	IV P5	15-02-2021	Interfacing seven segment displays	6	"
	"	IV P5	19-02-2020	alp for addition of largest/smallest order	6	"
	"	IV P5	22-02-2021	Addressing modes of 8051	6	"
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	"	IV P5	26-02-2021	Architecture of 8051	6	11
	"	IV P5	26-02-2021	Temparature mesurment (LM35)	6	"
19	Electronics	V P6	19-11-2020	Halfwave&Full wave and bridge rectifiers	18	S.KIRANMAYI

	"	V P6	19-11-2020	Types of filters	18	"
	"	V P6	05-12-2020	Transistor series voltage regulator	18	"
	"	V P6	05-12-2020	Block diagram of regulated power supply	18	"
	"	V P6	10-12-2020	Principle and working of SMPS	18	"
	"	V P6	10-12-2020	Half wave voltage doubler	18	"
	"	V P6	18-12-2020	Voltage tripler circuit diagram and working	18	"
	"	V P6	18-12-2020	Applications of voltage multipliers	18	"
	"	V P6	29-12-2020	SCR Half wave rectifier circuit and working	18	"
	"	V P6	29-12-2020	SCR full wave rectifier circuit and working	18	"
	"	V P7	19-11-2020	Types of electronic instruments	18	S.KIRANMAYI
	"	V P7	19-11-2020	Construction and working of analog, digital multimeter	18	"
	"	V P7	05-12-2020	Different types of oscilloscopes and uses	18	"
	"	V P7	05-12-2020	Types of SSDs	18	"
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	"	V P7	18-12-2020	Resistive ,capacitive&inductive transducers	18	"
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20	Electronics	VI	11-01-2021	Block diagram of 8051 microcontroller	14	S.KIRANMAYI
	"	VI	20-01-2021	Classification of instruction set	14	"
	"	VI	23-01-2021	Alp for addition of two 8 bit numbers	14	"
	"	VI	06-02-2021	memory organization of 8051	14	"
	"	VI	15-02-2021	Keyboard and display Interfacing	14	"
	"	VI	19-02-2020	Introduction to Embedded Systems	14	"
	"	VI	22-02-2021	160CXX PIC Controller	14	"
	"	VI	22-02-2021	ADC interfacing	14	"
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21	Electronics	Ι	02-02-2022	Difference between AC and DC	26	S.KIRANMAYI
	"	Ι	02-02-2022	Thevinen's theorem	26	"
	"	Ι	14-02-2022	Maximum power transfer Theorem	26	"

	"	Ι	14-02-2022	Star to deltaδ to star conversion	26	"
	"	Ι	15-02-2022	Low pass and high pass filters	26	"
	"	Ι	15-02-2022	Working and characteristics of JFET And MOSFET	26	"
	"	Ι	24-02-2022	Frequency response of RC & RL circuits	26	"
	"	Ι	24-02-2022	Half wave and full wave rectifiers	26	"
	"	Ι	01-03-2022	Light dependent resistor(LDR)	26	"
	"	Ι	05-03-2022	light Emitting Diode	26	"
22	Electronics	II	22-03-2022	Decimal, binary, octal, hexa decimal number system	26	S.KIRANMAYI
	"	Π	22-03-2022	NAND And NOR gates are universal gates	26	"
	"	Π	04-04-2022	1's,2's,9's,10's complements of addition, subtraction	26	"
	"	II	04-04-2022	Half adder and Full Adder	26	"
	"	Π	30-04-2022	De-morgan's theorem	26	"
	"	Π	30-04-2022	J-K Flip flop and master-slave flip flop	26	"
	"	II	06-05-2022	PLA and PAL	26	"
	"	Π	06-05-2022	Multiplexer and demultiplexer	26	"
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	"	II	09-05-2022	Encoder and Decoder	26	"
23	Electronics	III	02-02-2022	Block diagram of OP-Amp	26	S.KIRANMAYI
	"	III	02-02-2022	IC-555 Functional block diagram	26	"
	"	III	14-02-2022	OP-Amp as voltage Regulator	26	"
	"	III	14-02-2022	Need for modulation	26	"
	"	III	15-02-2022	OP-Amp as Inverting and non-inverting	26	"
	"	III	15-02-2022	Mathematical representation of FM wave	26	"
	"	III	24-02-2022	Diode detector	26	"
	"	III	24-02-2022	Super heterodyne AM Receiver	26	"
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24	Electronics	IV P4	22-03-2022	INTEL 8085 Architecture	24	S.KIRANMAYI
	"	IV P4	22-03-2022	Addressing modes of 8085	24	"
	"	IV P4	04-04-2022	alp for addition of two 8 bit numbers	24	"
	"	IV P4	04-04-2022	Pin diagram of 8085	24	"

	"	IV P4	30-04-2022	ARM Architecture and organization	24	"
	"	IV P4	30-04-2022	Pin description of 8086	24	"
	"	IV P4	06-05-2022	Serial communication interface (8251-USART)	24	"
	"	IV P4	06-05-2022	Architecture of 8086	24	"
	"	IV P4	09-05-2022	Program status word(PSW)	24	"
	"	IV P4	09-05-2022	16/32 bit processor	24	"
25	Electronics	V P5	02-02-2022	Functional Block diagram of Intel 8085	6	S.KIRANMAYI
	"	V P5	02-02-2022	Interrupts and priority concept	6	"
	"	V P5	14-02-2022	Instruction set of 8085	6	"
	"	V P5	14-02-2022	Timing diagram for opcode fetch	6	"
	"	V P5	15-02-2022	Alp for addition of two 8 bit numbers	6	"
	"	VP5	15-02-2022	Time delay using single and double register	6	"
	"	V P5	24-02-2022	2K X 8 ROM memory interfacing	6	"
	"	V P5	24-02-2022	Differences between I/O mapped and memory mapped I/O	6	"
	"	VP5	01-03-2022	Programmable peripheral devices 8255	6	"
	"	VP5	05-03-2022	stepper motor control Interface	6	"
	"	V P6	02-02-2022	Block diagram of communication system	6	S.KIRANMAYI
	"	V P6	02-02-2022	Noise in communication system	6	"
	"	V P6	14-02-2022	Transistor modulator	6	"
	"	V P6	14-02-2022	DSBSC generation	6	"
	"	VP6	15-02-2022	FET Reactance modulator	6	"
	"	V P6	15-02-2022	Comparision Between AM,FM and PM	6	"
	"	V P6	24-02-2022	Block diagram of AM Transmitter	6	"
	"	VP6	24-02-2022	Super heterodyne AM Receiver	6	"
	"	V P6	01-03-2022	Pulse amplitude modulation	6	"
	"	V P6	05-03-2022	Block diagram of AM Transmitter	6	"
26	Electronics	VI	22-03-2022	Block diagram of 8051 microcontroller	6	S.KIRANMAYI
	"	VI	22-03-2022	Classification of instruction set	6	"
	"	VI	04-04-2022	Alp for addition of two 8 bit numbers	6	"
	"	VI	04-04-2022	memory organization of 8051	6	"
	"	VI	30-04-2022	Keyboard and display Interfacing	6	"

	"	VI	30-04-2022	Introduction to Embedded Systems	6	"
	"	VI	06-05-2022	160CXX PIC Controller	6	"
	"	VI	06-05-2022	ADC interfacing	6	"
27	Electronics	Cluste	02-02-2022	Block diagram of measurement system	14	S.KIRANMAYI
	"	Cluste	02-02-2022	De Sauty bridge	14	"
	"	Cluste	14-02-2022	working of ammeter	14	"
	"	Cluste	14-02-2022	Basic Principles of phase locked loop	14	"
	"	Cluste	15-02-2022	Block diagram of Spectrophoto meter	14	"
	"	Cluste	15-02-2022	Function generator	14	"
	"	Cluste	24-02-2022	Principle, working of Ph meter	14	"
	"	Cluste	24-02-2022	Working of Thermometer	14	"
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	"	Ι	24-11-2022	Maximum power transfer Theorem	8	"
	"	Ι	24-11-2022	Star to deltaδ to star conversion	8	"
	"	Ι	08-12-2022	Low pass and high pass filters	8	"
	"	Ι	08-12-2022	Working and characteristics of JFET And MOSFET	8	"
	"	Ι	07-02-2023	Frequency response of RC & RL circuits	8	"
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	"	Π	15-05-2023	1's,2's,9's,10's complements of addition,subtraction	8	"
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	"	Π	22-05-2023	De-morgan's theorem	8	"
	"	Π	22-05-2023	J-K Flip flop and master-slave flip flop	8	"
	"	II	31-05-2023	PLA and PAL	8	"
	"	II	31-05-2023	Multiplexer and demultiplexer	8	"
	"	Π	08-06-2023	BCD to Seven Segment Display decoder	8	"
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23 CHEMISTRY	Sem - I	24-01-2022	Preparation of Alcholos	30	K.R.MANJULA
24 CHEMISTRY	Sem - I	25-01-2022	IR - Spectra Principle, Instrumentation, Fingerprint region, and Applications	30	K.R.MANJULA
25 CHEMISTRY	Sem - I	27-01-2022	Chromophore, Auxochrome, Bathochromic Shift	30	K.R.MANJULA
26 CHEMISTRY	Sem - I	28-01-2022	Preparation of Acetoacetic Ester by Claisen Condensation	30	K.R.MANJULA
27 CHEMISTRY	Sem - I	01-02-2022	Pinacol-Pinacoline Rearrangement Reaction	30	K.R.MANJULA
28 CHEMISTRY	Sem - I	02-02-2022	Reimer Tiemann Reaction and Kolbe Schmidt reaction	30	K.R.MANJULA
29 CHEMISTRY	Sem - I	03-02-2022	Preparation of Aldehydes and Ketones	30	K.R.MANJULA
30 CHEMISTRY	Sem - I	04-02-2022	PerkinReaction, Benzoin Condensation	30	K.R.MANJULA
31 CHEMISTRY	Sem - I	18-02-2022	Haloform reaction mechanism	30	K.R.MANJULA
32 CHEMISTRY S	Sem - I	13-03-2022	D/B Thermochemical and Photochemical Reactions	34	K.R.MANJULA
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36 CHEMISTRY S	Sem - I	11-05-2022	Nitro Alkanes Preparation, Properties, Tautomerism	34	K.R.MANJULA
37 CHEMISTRY	Sem - I	16-06-2022	Heterocyclic Compounds	34	K.R.MANJULA
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39 CHEMISTRY	Sem - I	02-07-2022	Organo Metallic Compounds	34	K.R.MANJULA
40 CHEMISTRY S	Sem - \	17-03-2023	Quantitative Analysis	30	K.R.MANJULA
41 CHEMISTRY	Sem - \	20-03-2023	Redox titrations and Complexometric titrations	30	K.R.MANJULA
42 CHEMISTRY	Sem - \	21-03-2023	Acid-Base titrations	30	K.R.MANJULA
43 CHEMISTRY S	Sem - \	29-03-2023	Indicators and theories of indicators	30	K.R.MANJULA
44 CHEMISTRY	Sem - \	24-04-2023	Solvent extractions	30	K.R.MANJULA
45 CHEMISTRY	Sem - \	23-06-2023	Ion exchange chromatography	30	K.R.MANJULA

VSR GOVERNMENT DEGREE & PG COLLEGE, MOVVA
DEPARTMENT OF CHEMISTRY
DATE: 13/6/23 CLASS: II BSC SEM: 14 TOPIC: Transport number
NAME OF THE LECTURER : Smt. M.NAGAPARAMESWARI
B. Naga Calitha, ASSIGNMENT-01 13/6/23
Q. what are transport numbers. Descoilible Hitlard's method for the determination of transport number.
ANSWER: Transport number: The fraction of the current carried
by an ion is called as its transport number". If No and no are the
transport number of anion and cotion despectively.
Transport number of _ Current carried by an anion
anion (No) Total current passed -through the solution
nd transport number = current confied by the cotion
of cation. (Ic) Total current passed through the solution.
The transport number of an ion depends upon the speed with
which it moves. Thus fall of concentration around the electrode & proportional to the speed of ions moving away from that electrode.
n = fau of concentration around the anode
Total fall of concentration.
milarly na = fall of concentration around the cothode
Total fall of concentration
and $n_0 + n_c = 1$.
Experimental determination of transport number by Hitlord'a

tethod: The Hittoria apparatus consists of two vertical gloss tubes connected by a U-tube in the middle. The end tubes

.

containing the anode and conthode compartments. The tube are provided with stop clock at the bottom. The electrodes are metallic and sealed into glow tubes. The apparatus 95 connected in series with a voltmeter, variable resistance a battery and also a milli ammeter in () + (+)+ Batlery suppose it is orequired to when milliamond Rheostori determine the transport number F > solution of silver and nitiate good in silver nitrate. The Hittorf's apparatus & filled with a standard solution of silver nitrate. Silver electrode & voltmeter are used in the process. voltmeter A steady current of 0.01 amperes / & passed for about 3 hours. During electrolysts, silver is transported from the anode compar -tment towards the cathode. In the anode compartment, nituale ions attacks silver anode. when it disolves to form silver nitrate. calculation:-Before electrolyssis of grams of Agnos sol containa c'gm of Ag After dectiolysts, H contains "b" gm of Ag Increase in the weight of Ag in anode compartment = b-c=w weight of silver deposited in the voltmeter = Wam fau in concentration due to the migration of Agtions = W-wgmg Transport number of Agt, nagt = W-w

so transport number of nitrate ion = 1-ngt

state &

USR GOVERNMENT DEGREE & PG COLLEGE, MOVVA
DEPARTMENT OF CHEMISTRY
DATE: 5/6/23 CLASS: IL 65(SEM: IV TOPIC: older of a rearlism.
NAME OF THE LECTURER: SML: M.NAGAPARAMESWARI
Name of the obvient :-
K. pula
a what is order of a reaction custifik varies methods used for
determining the other of reaction.
ANSWER: obter of a reaction custifik varies methods used for
ANSWER: obter of a reaction custifik varies methods used for
answer of the object of a reaction custifies after during the chemical
municity of atoms of the object of reaction.
ANSWER: object of a reaction custifies after during the chemical
multiple of atoms (a) molecular whose concentrations after during the chemical
multiple of atoms (a) molecular whose concentrations after during the sum of the power
change." Thus the osders of a reaction custifies atter during the sum of the power
if the concentrations in the rate equation.
Eg: D Detomposition of hydrogen perioxide is 1st orders.
2) Acid catalysid esters hydrolysis is 1st orders.
3) SN² seaction is 2nd orders of a reaction :-
10 Integration or substitution methods for the reaction is determined time
of reachings are mined and the produces of the reaction is determined time
substitutes in the kinetic equactions of Thest orders second orders and thind orders.

$$k_1 = \frac{2 \cdot 803}{2} (og = \frac{4(n+1)}{4(n+1)}) = \frac{2 \cdot 2}{4(n+1)} = \frac{2$$

. I a sophical method - it a standart to a la cloudred by deting log to -1) against a standard a st sic stratght for to abtained by subling any apatrice store and artist proved and artist practices. To senter the the concerning of reard orth are allowed. it a straight time is obtained by plotting log but against time also indered establish in which the concentration of surfaces are referred. log uny Jerand ander int order i such order 107 (0-+) in) Half life method or frictlogal change method :- the fine for soil change in concentration of the reactants is called time for holf change. It has been prived that the fime (1) sequired to complete half of the genetion is indmendent of initial premetation for a first order reaction. the fime (+) sequired to complete half of the reaction is inversely propertional to the initial renentration for a second orders reartica. The time (1) required to complete half of inversely proportional to square of initial concentration for athird order reachior. in general, the time (4) required to complete for order reaston puters Then +1 of $\frac{1}{q_1^{n-1}}$ and $\frac{1}{2} = \begin{bmatrix} \frac{a_2}{q_2} \end{bmatrix}^{n-1}$ where ness the order of remetion iv, as twould's is olation method :- This method involves by taking of all the reactants in large amounts except on e. so that concentration remains constant through out this change. The order of the reaction is determined with respect to prolated reactant which is not taken in large amount. The experiment is reparted by is olarling each reactant in turn. The the sum of the moder of determined total sum of the order obtained in each case gives the order of reaction Const offects + shele -> sfects + shely. In-the first exprement, the reactant Felg & raken in large quantity the order of reaction with respect to such is In the second exproment, the reactant sacle is taken in lage quantity the orders of bracklon with respaced to Feels is second oxis Thus over all order of reaction is (1+2)=31. Cthird order.

> V.S.R. Govt. Degree & P.G. College MOVVA-521135, Krishna Dt., A.P.